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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Jiren YUAN

Application No.: 09/672,803

Filing Date: September 28, 2000

Title: Versatile Charge Sampling Circuits

ATTN: DRAFTING BRANCH

Allowed: May 23, 2003

Group Art Unit: 2816

Examiner: My Trang Ton

Confirmation No.:

#16
WR

SUBMISSION OF FORMAL DRAWINGS

MAIL STOP ISSUE FEE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed please find 16 sheet(s) of formal drawings for review by the Patent and Trademark Office in connection with the Notice of Allowance mailed May 23, 2003. Should the enclosed drawing(s) require changes, it is respectfully requested that the Patent and Trademark Office notify the undersigned of same.

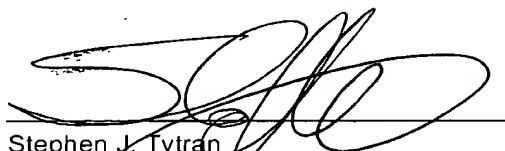
Respectfully submitted,

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Date: June 11, 2003

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